# ACT512FR72J8F667S
512MB FB-DIMM DDR2-667 ECC DIMM

## Description

ACTICA ACT512FR72J8F667S is a high speed 512MB FB-DIMM DDR2-667 ECC DIMM . It is designed for mission critical application memory solution. This DIMM includes Error Checking and Correcting (ECC) for maximum reliability. The modules is constructed using Samsung SDRAMs, and is fully compliant with JEDEC specifications. Decoupling capacitors are mounted on the PCB board for better signal integrity. The DIMM feature serial presence detect (SPD) based on a serial EEPROM device.

| Features | Value |
|---|---|
| ACTICA memory P/N | ACT512FR72J8F667S |
| CL-tRCD-tRP | 5-5-5 |
| ECC | Yes |
| RoHS compliant | Yes |
| Supply Voltage | 1.8V ± 0.1V |
| AMB Supply Voltage | 1.5V ± 0.05V |
| JEDEC standard | Yes |
| Operating Temperature* | 0 - 95 $^{0}$C |
| **Timing Parameter:** | |
| tCK (Clock Cycle Time) | 3ns |
| tRCD (Ras and Cas Delay) | 15ns |
| tRP (Row Precharge Time) | 15ns |
| tRAS (Row Active Time) | 45ns |
| **Operating Current:** | |
| Idd_Idle_0 (Idle Current, single or last DIMM) | 450 mA |
| Idd_Idle_1 (Idle Current, first DIMM) | 450 mA |
| Idd_Active_1 (Active Power) | 2,030 mA |
| Idd_Active_2 (Active Power, data pass through) | 450 mA |
| Idd_Training | 1,220 mA |

* At 85 - 95 °C operation temperature range, doubling refresh commands in frequency to a 32ms period ( tREFI=3.9 us ) is required, and to enter to self refresh mode at this temperature range
** not including AMB current

## Physical Dimension (drawing not in scale) Units : in Millimeters

(with heat spreader)