

**ACT512DU64Y8F400S**  
512MB DDR400 Unbuffered non-ECC DIMM

**Description**

ACTICA ACT512DU64Y8F400S is a high speed 512MB DDR400 Unbuffered non-ECC DIMM . It is designed for mission critical application memory solution. The modules is constructed using Samsung SDRAMs, and is fully compliant with JEDEC specifications. Decoupling capacitors are mounted on the PCB board for better signal integrity. The DIMM feature serial presence detect (SPD) based on a serial EEPROM device using the 2-pin I2C protocol.

Features	Value
ACTICA memory P/N	ACT512DU64Y8F400S
CL-tRCD-tRP	3-3-3
RoHS compliant	Yes
Supply Voltage	2.6V ± 0.1V
JEDEC standard	Yes
Operating Temperature*	0 - 90 °C
<b>Timing Parameter:</b>	
tCK (Clock Cycle Time)	5ns
tRCD (Ras and Cas Delay)	15ns
tRP (Row Precharge Time)	15ns
tRAS (Row Active Time)	40ns
tIH (Input Hold Time)	0.6ns
tIS (Input Setup Time)	0.6ns
<b>Operating Current:</b>	
IDD0 (Operating one bank active-precharge current)	960 mA
IDD1 (Operating one bank active-read-precharge current)	1,200 mA
IDD2P (Precharge power-down current)	40 mA
IDD3N (Active standby current)	480 mA
IDD6 (Self refresh current)	40 mA

**Physical Dimension** (drawing not in scale)

Units : in Millimeters

